

Notice of References Cited

Application No.
08/720,693

Applicant(s)
Kao et al

Examiner
George Fourson

Group Art Unit
2823

Page 1 of 1

U.S. PATENT DOCUMENTS

	DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS
A					
B					
C					
D					
E					
F					
G					
H					
I					
J					
K					
L					
M					

FOREIGN PATENT DOCUMENTS

	DOCUMENT NO.	DATE	COUNTRY	NAME	CLASS	SUBCLASS
N						
O						
P						
Q						
R						
S						
T						

NON-PATENT DOCUMENTS

	DOCUMENT (Including Author, Title, Source, and Pertinent Pages)	DATE
U	Wolf, S. et al, "Silicon Processing For the VLSI Era: Vol.1, Process Technology", Lattice Press, pp.323-324	1986
V		
W		
X		